LINE 1 - Smooth IP Chargeability from Resistive Half-space (null conductivity) Reference


LINE 1 - Smooth Unrorated PW TMTE Inversion from a 5000 ohm-m Resistive Half-space


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LINE 1 - Smooth DC Resistivity



LINE 1 - Smooth IP Chargeability from DC Resistivity Reference


LINE 2 - Smooth IP Chargeability from Resistive Half-space (null conductivity) Reference


LINE 2 - Smooth Unrorated PW TMTE Inversion from a 5000 ohm-m Resistive Half-space
(2000

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LINE 3 - Smooth IP Chargeability from Resistive Half-space (null conductivity) Reference


LINE 3 - Smooth Unrorated PW TMTE Inversion from a 5000 ohm-m Resistive Half-space
(2500

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